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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/806,402	03/23/2004	Shingo Yamaguchi	250119US-2 DIV	2350
22850	7590	03/23/2006	EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.			COUSO, YON JUNG	
1940 DUKE STREET			ART UNIT	
ALEXANDRIA, VA 22314			PAPER NUMBER	

2624

DATE MAILED: 03/23/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/806,402

Applicant(s)

YAMAGUCHI ET AL.

Examiner

Yon Couso

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 35-42 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 35-42 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>3/23/04</u> . | 6) <input type="checkbox"/> Other: _____ |

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 35-42 are rejected under 35 U.S.C. 102(e) as being anticipated by Hicok et al (US Patent No. 6,266,753).

As for claim 35, Hicok teaches a system for forming an image having a first end and a second end which is perpendicular to the first end, comprising: a bus (figure 6 and column 7, lines 12-21); a first memory device connected to the bus (610 in figure 6); a second memory device connected to the bus (620 in figure 6); and a virtual memory controller connected to the bus and configured to swap image data between the first memory device and the second memory device (630 in figure 6, column 6, lines 4-12, and column 7, lines 22-49).

As for claim 36, Hicok teaches that the first memory device comprises: a non-paged memory; and a paged memory (column 6, lines 6-12).

As for claim 37, Hicok teaches that the non-paged memory is configured to host a portion of an operating system or an operating system object (column 6, lines 4-12).

As for claim 38, Hicok teaches that the operating system maintains a page-table (column 6, lines 4-52).

As to claim 39, Hicok teaches that the operating system is configured to use the page-table to service page-faults (page 6, lines 42-52)

As for claim 40, Hicok teaches that the non-paged memory is configured to host image data (column 4, lines 38-46).

As for claim 41, Hicok teaches the system further comprising: the direct memory access read buffer connected to the bus (690 in figure 6); a direct memory access write buffer connected to the bus (690 in figure 6); and a control and status register buffer connected to the bus (630, 640 and 650 in figure 6).

As for claim 42, Hicok teaches that one of a plot processor connected to the bus and a scan processor connected to the bus (column 2, lines 15-31).

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Georg et al, Pechter, Karkhanis et al, Politis, Haba, and Wilson, Jr are also cited.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yon Couso whose telephone number is (571) 272-7448. The examiner can normally be reached on Monday through Friday from 8:30 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jingge Wu, can be reached on (571) 272-7429. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

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Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



VON J. COUSO
PRIMARY EXAMINER

YJC

March 7, 2006